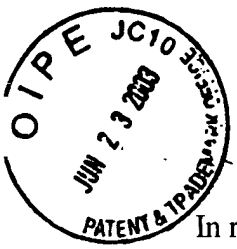


# 15 B  
7-1-03  
Mallin



PATENT Attorney Docket No. SEL 174

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of: )  
Shunpei Yamazaki, et al. )  
Serial No.: 09/544,801 )  
Filed: April 7, 2000 )  
Examiner: Falzi Erdem )  
Art Unit: 2826 )  
For: SEMICONDUCTOR DEVICE AND )  
METHOD FOR FABRICATING THE )  
SAME )

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to:  
  
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**AMENDMENT B**

Dear Sir:

In response to the Office Action mailed February 18, 2003, please amend the above-identified patent application as follows.

**In the Specification:**

Please amend page 8, line 10, to read as follows:

Fig. 8A to 8C are cross-sectional views of fabrication steps for a drive circuit TFT;

B1

[Please amend page 8, line 11, to read as follows:]

Fig. 9A to 9C are cross-sectional views of fabrication steps for a pixel TFT;